

Abstracts

A direct conversion transceiver for multi-band GSM application

J. Strange and S. Atkinson. "A direct conversion transceiver for multi-band GSM application." 2000 Radio Frequency Integrated Circuits (RFIC) Symposium 00. (2000 [RFIC]): 25-28.

The use of direct conversion receiver topologies has been marked by many technical problems, particularly when used in a TDMA environment. This paper describes a receiver architecture that overcomes many of the traditional problems associated with direct conversion. This architecture has been applied in the design of a GSM multi-band transceiver that also features a development of the offset PLL transmitter together with a fast locking fractional-N synthesizer.

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